

## Simple on-line observation and compensation of dead-time effects

S. M. Jung<sup>1</sup>, C. B. Park<sup>1</sup>, J. H. Kim<sup>1</sup>, S. H. Lee<sup>1</sup> & H. W. Kim<sup>2</sup>

<sup>1</sup>*Advanced Traction and Noncontact Feeding System Research Team, Korea Railroad Research Institute, Korea*

<sup>2</sup>*Department of Control and Instrumentation Engineering, Korea National University of Transportation, Korea*

### Abstract

Generally, there exist voltage differences between the voltage commands and the output voltages of the three-phase pulse-width modulated (PWM) voltage-source inverter (VSI), which are caused by the intended dead-time to prevent the arm-short of a leg and the nonlinearities of switching devices. These voltage differences (or voltage distortion) affect the current distortion, torque pulsations, and degradation of control performances. To overcome these drawbacks, a simple and low-cost observation and compensation method of the voltage distortion is proposed. The proposed method is employing model reference adaptive system (MRAS) techniques and does not require any additional hardware circuits such as voltage sensors. The voltage distortions caused by the nonlinearities of the switching devices are estimated by a voltage distortion observer which is based on the electrical model of the PMSM. And the estimated voltage distortions are fed-forward to compensate the voltage commands. In comparison with the previous existing methods, the proposed method has a simple structure and good performances. Also, it can be implemented without any extra devices such as voltage sensors and the computing effort is very small. The execution of the algorithm can be easily embedded in the existing systems without major modification. To show the effectiveness of the proposed method, the experiments are carried out for the digitally controlled PMSM drive system. The experimental results verify the validity of the proposed method and show that the proposed method gives good performance and practical value.

*Keywords: dead-time effects, voltage-source inverter, voltage distortion, MRAS.*



# 1 Introduction

To control traction motor for railway, the pulse width modulated (PWM) voltage source inverter (VSI) is generally used. In general, there exist voltage differences between the voltage commands and the output voltages of the PWM VSI, which are caused by the intended dead-time to prevent the arm-short of a leg and the nonlinearities of switching devices such as voltage drops, turn on/off time, and output voltage transition time. This voltage distortion is severe at low speed range and causes current distortion which may result in unacceptable torque pulsations and degradation of control performance.

To observe and compensate the dead-time effects, various methods have been proposed in the literature [2–7]. However, some require the hardware equipment such as voltage sensors, or some are quite complex to implement and apply the algorithms. To overcome these drawbacks, a simple online observation and compensation method of the dead-time effects is proposed in this paper. The proposed method does not require any additional hardware equipment such as voltage sensors and quite simple to embed the algorithm to an existing system.

# 2 Dead-time effects of PWM VSI

Figure 1 shows the basic configuration of phase A leg in a three-phase PWM VSI. When the system is in the ideal case as can be seen in figure 1(a), the terminal voltage of phase A,  $v_{a0}$ , is determined by the phase current  $i_{as}$  and the switching function ( $S_a$ ) of  $Q_{aU}$  and  $Q_{aL}$ . If the switching function is 1, which means that  $Q_{aU}$  is turned on and  $Q_{aL}$  is turned off, the terminal voltage of phase A is equal to  $V_{dc}/2$ , where  $V_{dc}$  is the DC-link voltage. If the switching function is 0, which means that  $Q_{aU}$  is turned off and  $Q_{aL}$  is turned on,  $v_{a0} = -V_{dc}/2$ . The possible terminal voltages according to the switching function and the direction

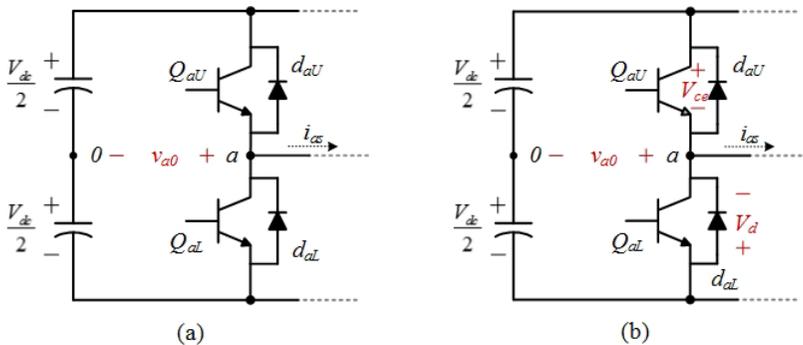


Figure 1: Basic configuration of phase A leg in a three-phase PWM VSI. (a) Ideal case; (b) Real case including characteristics of the switching devices.

of phase current in the normal condition are represented in table 1. Unlike the ideal case, however, the real switches in the PWM VSI which have the non-ideal characteristics results in changing the corresponding terminal voltage when the phase current  $i_{as}$  is positive and the switching function ( $S_a$ ) is 1. In this case, the terminal voltage of phase A is not equal to  $V_{dc}/2$ , but equal to  $V_{dc}/2 - V_{ce}$ , where  $V_{ce}$  is the forward voltage drop of the body diode in the switch  $Q_{aU}$ . The equivalent circuit of the real PWM VSI is shown in figure 1(b), and the corresponding terminal voltages are represented in table 2.

Table 1: Terminal voltages of phase A in ideal case.

	$S_a = 1$	$S_a = 0$
$i_{as} \geq 0$	$v_{a0} = \frac{V_{dc}}{2}$	$v_{a0} = -\frac{V_{dc}}{2}$
$i_{as} < 0$	$v_{a0} = \frac{V_{dc}}{2}$	$v_{a0} = -\frac{V_{dc}}{2}$

Table 2: Terminal voltages of phase A in real case.

	$S_a = 1$	$S_a = 0$
$i_{as} \geq 0$	$v_{a0} = \frac{V_{dc}}{2} - V_{ce}$	$v_{a0} = -\frac{V_{dc}}{2} - V_d$
$i_{as} < 0$	$v_{a0} = \frac{V_{dc}}{2} + V_d$	$v_{a0} = -\frac{V_{dc}}{2} + V_{ce}$

From the above analysis, it is obvious that the phase voltages between the ideal case and real case may have the voltage deviations in the steady state. Based on this fact, the proposed observation and compensation method of dead-time effects indirectly observes these voltage deviations using the analytical model of the PWM VSI.

To take a close look at the effect of the open-circuit fault of a switch on the phase voltages, the knowledge of the relationship between the terminal voltages and the phase voltages is required. This relationship can be represented as follows [1]:

$$\begin{bmatrix} v_{a0} \\ v_{b0} \\ v_{c0} \end{bmatrix} = \begin{bmatrix} v_{as} \\ v_{bs} \\ v_{cs} \end{bmatrix} + v_{s0} \quad (1)$$

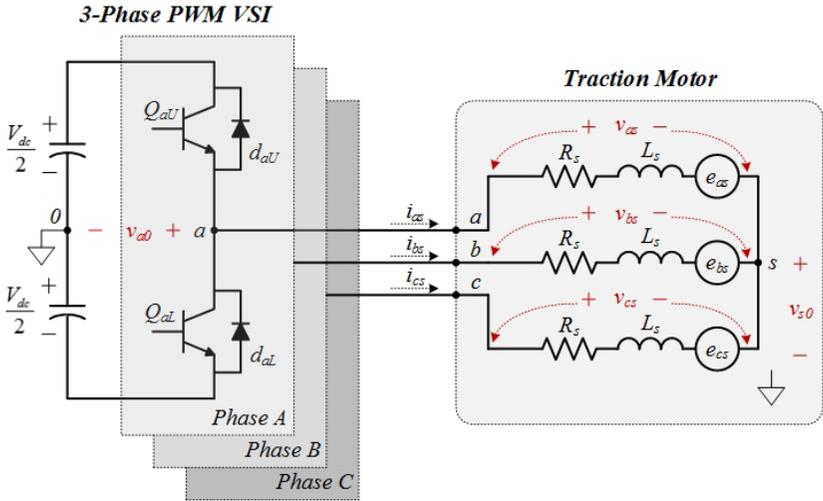


Figure 2: Relationship between terminal voltages and phase voltages.

where  $v_{as}$ ,  $v_{bs}$ , and  $v_{cs}$  are the phase voltages and  $v_{s0}$  is the neutral to centre voltage (as shown in figure 2). In a three-phase three-wire system, the following condition by Kirchoff's law is satisfied as

$$i_{as} + i_{bs} + i_{cs} = 0. \tag{2}$$

Also, the sum of each phase back-EMF is equal to zero at any instant under the assumption that the air-gap magnetic flux distribution is a sinusoid. From eqn. (2) and this assumption, the following condition is satisfied for the PMSM as

$$v_{as} + v_{bs} + v_{cs} = R_s (i_{as} + i_{bs} + i_{cs}) + L_s \frac{d}{dt}(i_{as} + i_{bs} + i_{cs}) + (e_{as} + e_{bs} + e_{cs}) = 0 \tag{3}$$

where  $R_s$  is a stator resistance,  $L_s$  is a stator inductance, and  $e_{as}$ ,  $e_{bs}$ , and  $e_{cs}$  represent the corresponding phase back-EMFs, respectively. From eqn. (1) to eqn. (3), the neutral to centre voltage,  $v_{s0}$ , becomes

$$v_{s0} = \frac{1}{3}(v_{a0} + v_{b0} + v_{c0}). \tag{4}$$

Therefore, the relationship between the terminal voltages and the phase voltages can be represented as follows:



$$\begin{bmatrix} v_{as} \\ v_{bs} \\ v_{cs} \end{bmatrix} = \frac{1}{3} \begin{bmatrix} 2 & -1 & -1 \\ -1 & 2 & -1 \\ -1 & -1 & 2 \end{bmatrix} \begin{bmatrix} v_{a0} \\ v_{b0} \\ v_{c0} \end{bmatrix}. \quad (5)$$

On the other hand, the dead-time effects can be represented by a deviation value from the terminal voltages of the PWM VSI. If  $\Delta v_{a0}$ ,  $\Delta v_{b0}$ , and  $\Delta v_{c0}$  are considered as representing the voltage deviations due to the dead-time effects, the phase voltages are represented as follows:

$$\begin{bmatrix} v_{as\_real} \\ v_{bs\_real} \\ v_{cs\_real} \end{bmatrix} = \frac{1}{3} \begin{bmatrix} 2 & -1 & -1 \\ -1 & 2 & -1 \\ -1 & -1 & 2 \end{bmatrix} \begin{bmatrix} v_{a0} \pm \Delta v_{a0} \\ v_{b0} \pm \Delta v_{b0} \\ v_{c0} \pm \Delta v_{c0} \end{bmatrix}. \quad (6)$$

where  $v_{as\_real}$ ,  $v_{bs\_real}$ , and  $v_{cs\_real}$  are the phase voltages in the real case. After some calculations, eqn. (6) can be expressed as

$$\begin{aligned} \begin{bmatrix} v_{as\_real} \\ v_{bs\_real} \\ v_{cs\_real} \end{bmatrix} &= \begin{bmatrix} v_{as} \\ v_{bs} \\ v_{cs} \end{bmatrix} + \frac{1}{3} \begin{bmatrix} 2 & -1 & -1 \\ -1 & 2 & -1 \\ -1 & -1 & 2 \end{bmatrix} \begin{bmatrix} \pm \Delta v_{a0} \\ \pm \Delta v_{b0} \\ \pm \Delta v_{c0} \end{bmatrix} \\ &= \begin{bmatrix} v_{as} \\ v_{bs} \\ v_{cs} \end{bmatrix} + \begin{bmatrix} \Delta v_{as\_dist} \\ \Delta v_{bs\_dist} \\ \Delta v_{cs\_dist} \end{bmatrix} \end{aligned} \quad (7)$$

where  $\Delta v_{as\_dist}$ ,  $\Delta v_{bs\_dist}$ , and  $\Delta v_{cs\_dist}$  are the phase voltage deviations introduced by the dead-time effects. As can be seen in eqn. (7), the phase voltage in the real case can be divided into two parts. The first term of eqn. (7),  $v_{ks}$  ( $k = \{a, b, c\}$ ), are the ideal phase voltages and the second term of eqn. (7),  $\Delta v_{ks\_dist}$  ( $k = \{a, b, c\}$ ), are the voltage deviations due to the dead-time effects. These phase voltage deviations can be considered as 'voltage distortions' induced by the dead-time effects and observed from the machine parameters, which is discussed in the following.

The voltage distortions in the  $abc$  frame can be transformed to the rotor reference frame by using the relationship [1] as

$$\begin{bmatrix} v_{q\_real} \\ v_{d\_real} \end{bmatrix} = T(\theta_e) \begin{bmatrix} v_{as\_real} \\ v_{bs\_real} \\ v_{cs\_real} \end{bmatrix}, \tag{8}$$

$$T(\theta_e) = \frac{2}{3} \begin{bmatrix} \cos \theta_e & \cos\left(\theta_e - \frac{2\pi}{3}\right) & \cos\left(\theta_e + \frac{2\pi}{3}\right) \\ \sin \theta_e & \sin\left(\theta_e - \frac{2\pi}{3}\right) & \sin\left(\theta_e + \frac{2\pi}{3}\right) \end{bmatrix} \tag{9}$$

where  $\theta_e$  is the electrical angular position of the rotor. Using from eqn. (7) to eqn. (9), the voltage distortions in the rotor reference frame can be represented as

$$\begin{bmatrix} v_{q\_real} \\ v_{d\_real} \end{bmatrix} = T(\theta_e) \begin{bmatrix} v_{as} + \Delta v_{as\_dist} \\ v_{bs} + \Delta v_{bs\_dist} \\ v_{cs} + \Delta v_{cs\_dist} \end{bmatrix} = \begin{bmatrix} v_q \\ v_d \end{bmatrix} + \begin{bmatrix} \Delta v_{q\_dist} \\ \Delta v_{d\_dist} \end{bmatrix} \tag{10}$$

where  $v_{q\_real}$  and  $v_{d\_real}$  are the  $q$ - and  $d$ -axis voltages in the real case,  $v_q$  and  $v_d$  are the ideal  $q$ - and  $d$ -axis voltages, and  $\Delta v_{q\_dist}$  and  $\Delta v_{d\_dist}$  are the  $q$ - and  $d$ -axis voltage deviations or distortions due to the dead-time effects. The stator voltages in the rotor reference frame after the fault occurrence can also be represented as two parts. One is the ideal  $q$ - and  $d$ -axis voltages and the other is the voltage deviations caused by the dead-time effects.

### 3 Proposed on-line observation and compensation of dead-time effects

The current dynamics of a PMSM including the voltage distortions caused by the dead-time effects can be represented as follows [1]:

$$\begin{bmatrix} \frac{di_{q\_real}}{dt} \\ \frac{di_{d\_real}}{dt} \end{bmatrix} = \begin{bmatrix} -\frac{R_s}{L_s} & -\omega_e \\ \omega_e & -\frac{R_s}{L_s} \end{bmatrix} \begin{bmatrix} i_{q\_real} \\ i_{d\_real} \end{bmatrix} + \frac{1}{L_s} \begin{bmatrix} v_q + \Delta v_{q\_dist} \\ v_d + \Delta v_{d\_dist} \end{bmatrix} + \begin{bmatrix} -\frac{\lambda_m \omega_e}{L_s} \\ 0 \end{bmatrix} \tag{11}$$

where  $i_{q\_real}$  and  $i_{d\_real}$  are the  $q$ - and  $d$ -axis currents,  $\omega_e$  is the electrical rotor angular speed,  $\lambda_m$  is the flux linkage established by the permanent magnet, respectively. As can be seen in eqn. (11), the motor currents are affected by the voltage distortions caused by the dead-time effects.



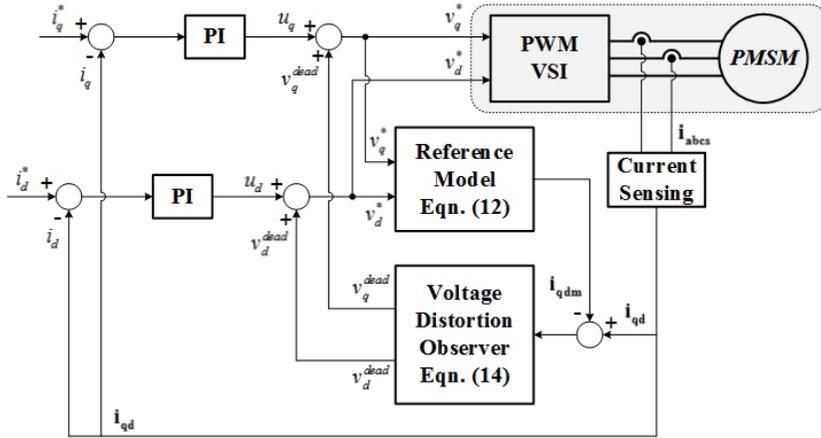


Figure 3: Current control block diagram using the proposed voltage distortion observer for the compensation of the dead-time effects.

For the reference model of the MRAS, it is assumed that the voltage distortions due to the dead-time effects are zero and the system is in the ideal case. In this condition, the calculated current dynamics using the nominal parameters can also be represented as follows:

$$\begin{bmatrix} \frac{di_{qm}}{dt} \\ \frac{di_{dm}}{dt} \end{bmatrix} = \begin{bmatrix} -\frac{R_{s0}}{L_{s0}} & -\omega_e \\ \omega_e & -\frac{R_{s0}}{L_{s0}} \end{bmatrix} \begin{bmatrix} i_q \\ i_d \end{bmatrix} + \frac{1}{L_{s0}} \begin{bmatrix} v_q^* \\ v_d^* \end{bmatrix} + \begin{bmatrix} -\frac{\lambda_{m0}\omega_e}{L_{s0}} \\ 0 \end{bmatrix} \quad (12)$$

where  $v_q^*$  and  $v_d^*$  are the  $q$ - and  $d$ -axis stator voltage commands,  $i_{qm}$  and  $i_{dm}$  are the  $q$ - and  $d$ -axis currents of the model, respectively, and the subscript “0” represents the nominal value.

From eqn. (11) and eqn. (12), the voltage distortions in the rotor reference frame caused by the dead-time effects can be obtained as follows:

$$\begin{aligned} v_q^{dead} &= v_q^* - v_{q\_real} = -L_s \left( \frac{di_{q\_real}}{dt} - \frac{di_{qm}}{dt} \right) \\ v_d^{dead} &= v_d^* - v_{d\_real} = -L_s \left( \frac{di_{d\_real}}{dt} - \frac{di_{dm}}{dt} \right) \end{aligned} \quad (13)$$

where it is assumed that the nominal parameters  $R_{s0}$ ,  $L_{s0}$ , and  $\lambda_{m0}$  are identical to the real values  $R_s$ ,  $L_s$ , and  $\lambda_m$ , respectively. The average voltage distortions over the  $k^{\text{th}}$  PWM step can be derived from eqn. (13) as follows:

Table 3: Specifications of the test PMSM.

Rated Power	750 [W]	# of poles	8
Rated Torque	2.4 [Nm]	Rated Speed	3000 [r/min]
Phase Resistance	0.49 [ $\Omega$ ]	Rated Current	6.0 [A]
Linkage Flux	0.0667 [Wb]	Phase Inductance	6.9 [mH]

Table 4: Specifications of the three-phase PWM VSI.

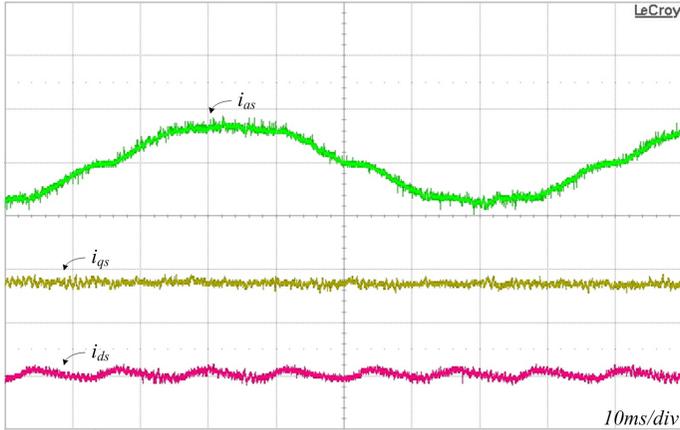
DC-link Voltage	300 [V]	Switching Frequency	11 [kHz]
Dead-time	2.8 [usec]	Switching Device	IGBT
Turn-on Time <sup>†</sup>	25 [nsec]	Turn-off Time <sup>†</sup>	115 [nsec]
Saturation Voltage <sup>†</sup>	2.5 [V]	Forward Voltage <sup>†</sup>	1.95 [V]

<sup>†</sup>Fairchild datasheet (FGH40N60SFD).

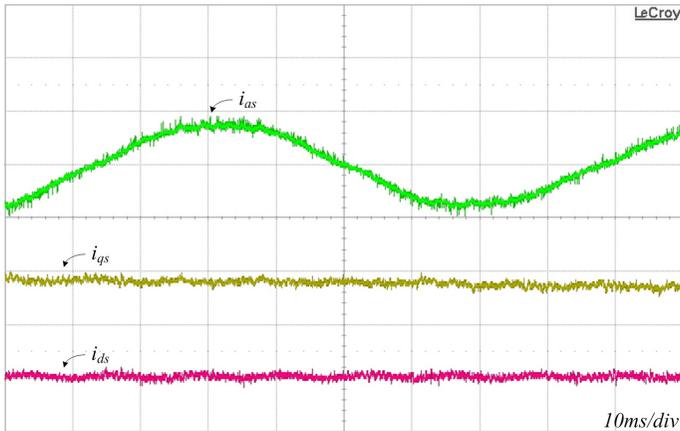
$$\begin{aligned}
 v_q^{dead}(k) &= v_q^*(k) - v_{q\_real}(k) = -L_s \left( \frac{i_{q\_real}(k) - i_{qm}(k)}{T_s} \right) \\
 v_d^{dead}(k) &= v_d^*(k) - v_{d\_real}(k) = -L_s \left( \frac{i_{d\_real}(k) - i_{dm}(k)}{T_s} \right)
 \end{aligned} \tag{14}$$

The model currents  $i_{qm}(k)$  and  $i_{dm}(k)$  in eqn. (14) can be obtained from the discrete form of eqn. (12).

By the feed-forward compensation manner, the observed dead-time effect in eqn. (14) is added to the voltage commands. The voltage commands with the compensation of the dead-time effect are represented as follows:



(a)



(b)

Figure 4: Experimental results of the current waveforms. (a) Without dead-time compensation. (b) With the proposed voltage distortion observer.

$$\begin{aligned} v_q^*(k) &= u_q(k) + e_q(k) + v_q^{dead}(k) \\ v_d^*(k) &= u_d(k) + e_d(k) + v_d^{dead}(k) \end{aligned} \quad (15)$$

where  $u_q$  and  $u_d$  are the results of decoupled PI current control in  $q$ - and  $d$ -axis, respectively,  $e_q = L_s \omega_e i_d + \lambda_m \omega_e$ ,  $e_d = -L_s \omega_e i_q$ , and  $v_q^{dead}$  and  $v_d^{dead}$  are the compensation voltages in  $q$ - and  $d$ -axis from eqn. (14), respectively. The overall block diagram of the compensation for the dead-time effect is shown in figure 3.

In order to confirm the feasibility of the fault diagnosis algorithm, the experiments have been realized scaled-down model. The parameters related to the test motor and three-phase PWM converter are represented in table 3 and table 4, respectively. The converter is composed of six IGBTs, FGH40N60SFD from Fairchild, Corp. The switching frequency of the converter is  $11\text{kHz}$ . All the control laws proposed in this dissertation have been realized by using a single microcontroller unit TMS320F28335 from Texas Instruments, Inc. The sampling rate of phase currents is identical to the switching frequency.

The experimental result for the compensation of the dead-time effect is shown in figure 4. The phase A current waveform without the dead-time compensation is distorted, but this is almost sinusoids with the proposed compensation method. Therefore, the proposed method can be used for the dead-time.

## 4 Conclusion

In this paper, a simple online observation and compensation method of the dead-time effects is proposed, which will improve the current distortion of the PWM VSI. As a first step, the dead-time effects is analysed, which changes the corresponding terminal voltage and introduces the voltage deviations or voltage distortions to each phase voltage. By observing these voltage distortions achieved by the simple MRAS-based voltage distortion observer, the current distortion can be significantly reduced. The proposed compensation method is also quite simple so that the algorithm can be easily embedded in the existing systems without major modifications. To show the effectiveness of the proposed method, the experiments are carried out for the digitally controlled drive system. The results verify the validity of the proposed method and show that the proposed method gives the good performance and practical value.

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